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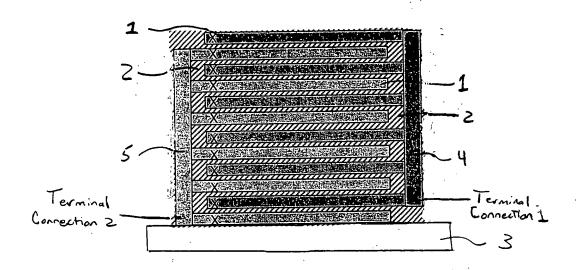


Fig. 1

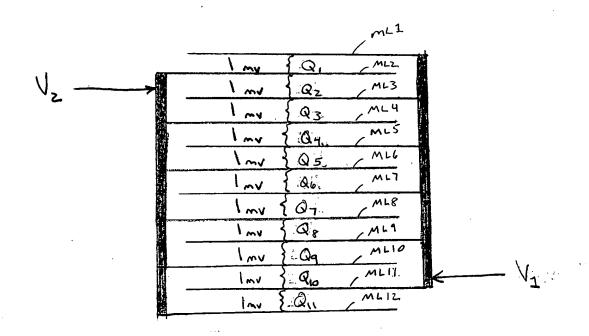
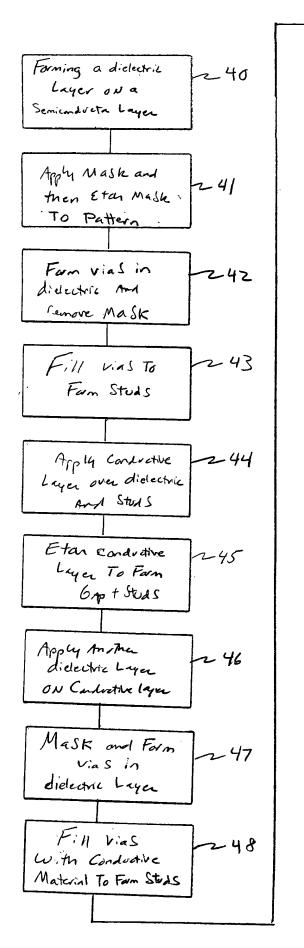


Fig. 2

First Terminal = NV Q = C V

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Deposit Another
Conductive layer over
Dielectric layer

Deposit Additional
dielectric and
Conductive layers
AS needled

Fig. 4

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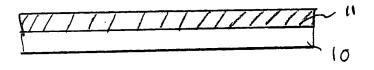


Fig. Sa)

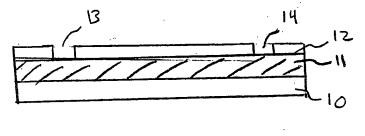


Fig. 5(b)

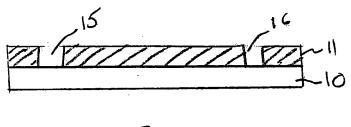
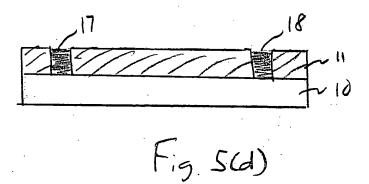
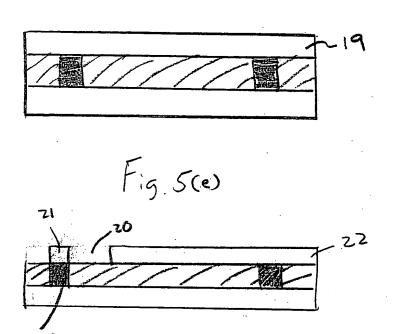


Fig. 5(c)





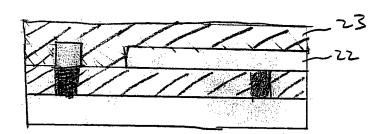


Fig. 5(f)

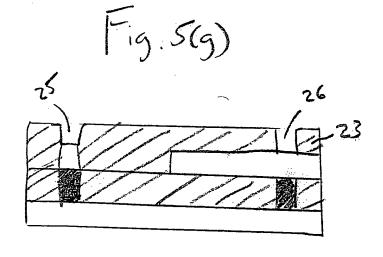


Fig. 5(h)

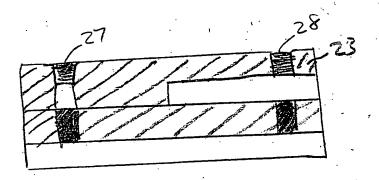


Fig. 5(1)

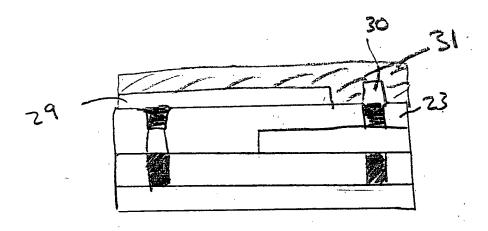
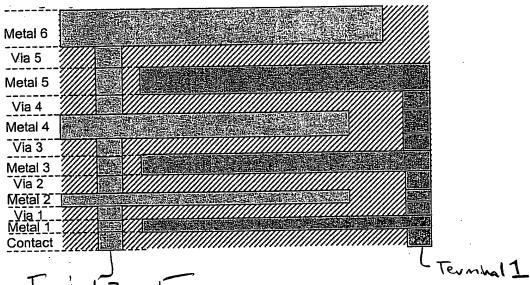


Fig. 5(i)



Firminal 2 Fig. 5(k)

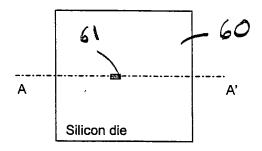


Fig.6(a)

### **Equivalent capacitor**

Fig.6(6)

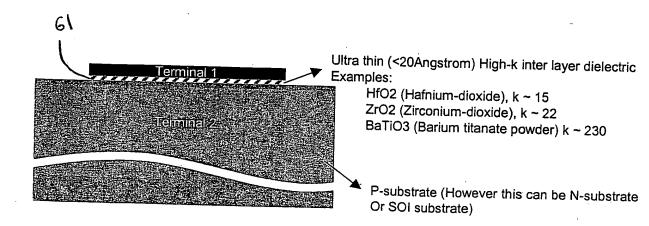


Fig. 6(1)

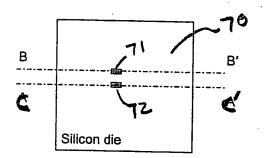
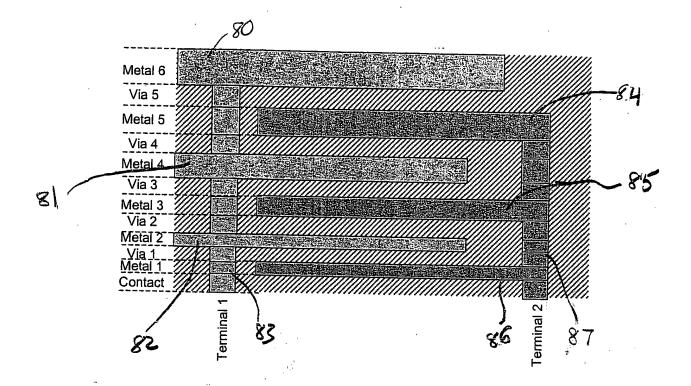


Fig.7(a)

Fig.7(b)



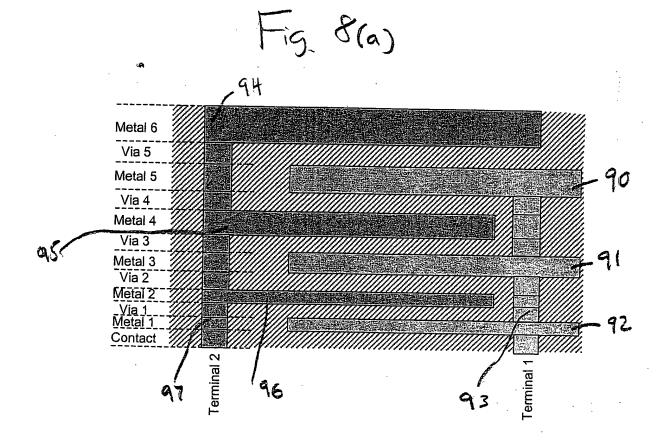


Fig. 8(b)

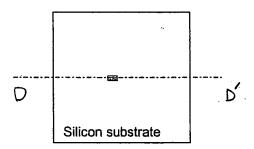


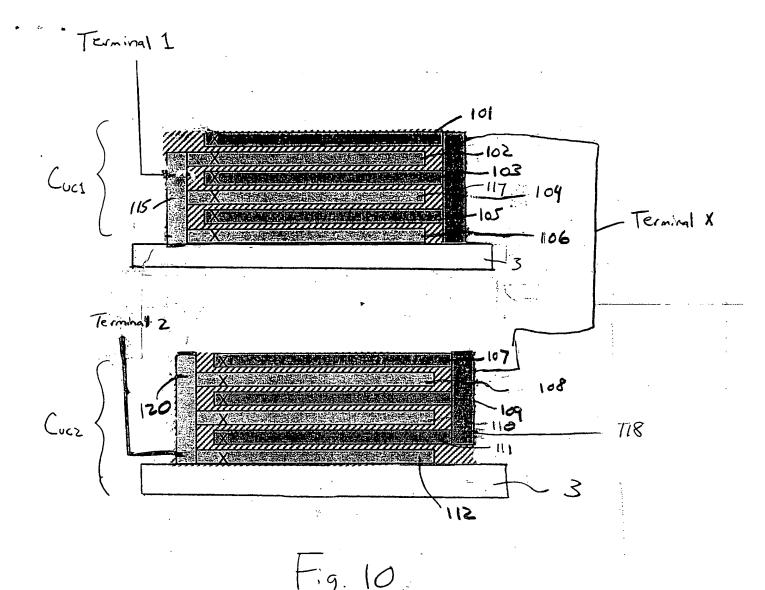
Fig. 9(a)

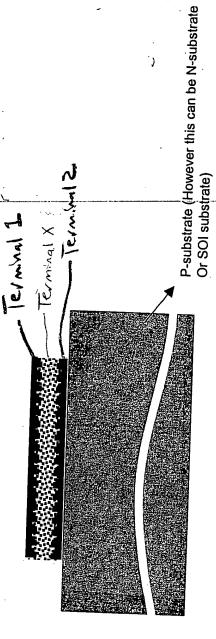
## **Equivalent capacitor**

Terminal 1
$$C_{UC_1} = V_1$$

$$V_2$$
Terminal 2

Fig. 9(b)





Electrode (Terminal 1) – Example: Porous carbon or doped silicon. Can be conductive carbon or silicon nanotube. Electrolyte (Terminal x) – Example: Potassium hydroxide Electrode (Terminal 2) – Example: Porous carbon or doped silicon. Can be conductive carbon or silicon nanotube.

100

Example cross section showing stacking of multiple substrate to increase the capacitance per unit area

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Terminal 1

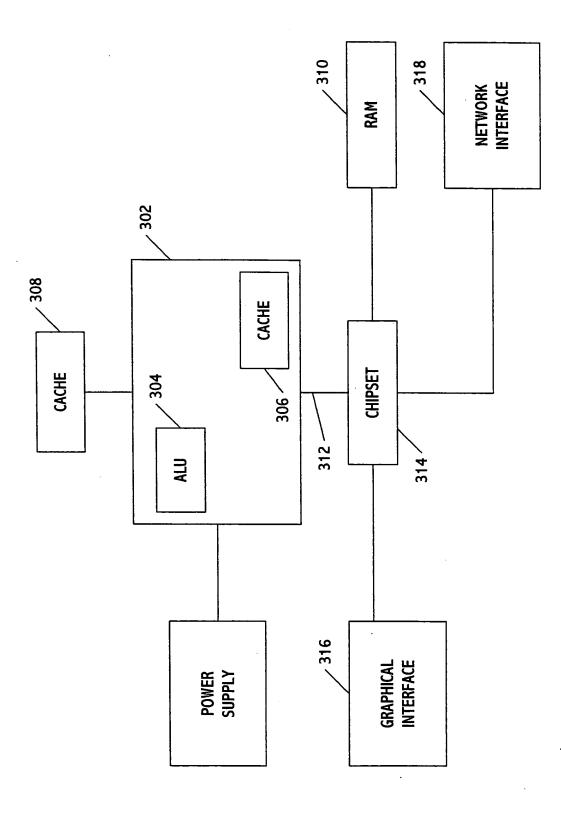


FIG.13